

DISPLAY APPARATUS WITH REDUCED NOISE EMISSION AND  
DRIVING METHOD FOR THE DISPLAY APPARATUS

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ABSTRACT OF THE DISCLOSURE

10 A display apparatus has a clock generating circuit,  
a drive waveform generating circuit, and a display panel.  
The drive waveform generating circuit is used to generate  
a drive waveform by using a clock from the clock  
generating circuit, and the display panel is used to  
display an image in accordance with the drive waveform.  
15 The clock generating circuit generates a clock whose  
frequency varies continuously. The drive waveform  
generating circuit drives the display panel by outputting  
a drive waveform whose frequency varies in accordance  
with the frequency varying clock so as to spread out  
20 noise that the display panel emits. Therefore, peak  
values of the noise can be reduced.